

SPECIFICATION FOR APPROVAL

Description	680nm 5mW VCSEL Laser Diode
Part No	DRS680-0005-08-5730E
Date	2023-03-30

Producer	Engineering Dept.	Business department	Approval
Zhang Jun	Tan Shangjun	Ouyang Song	Chen Mingxiang

Customer acknowledges Results		



Exterior

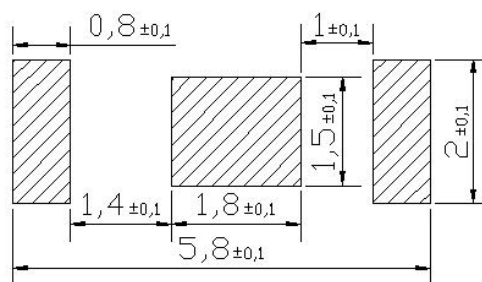
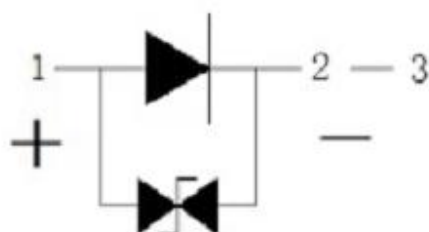
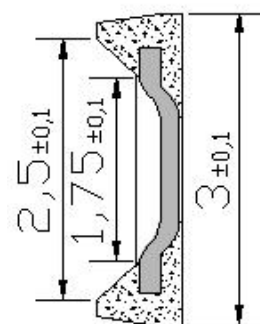
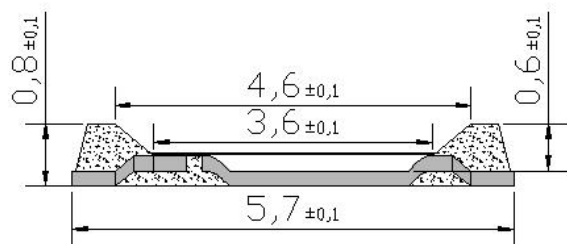
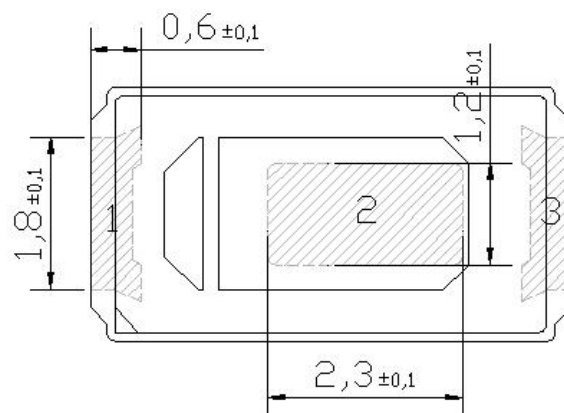
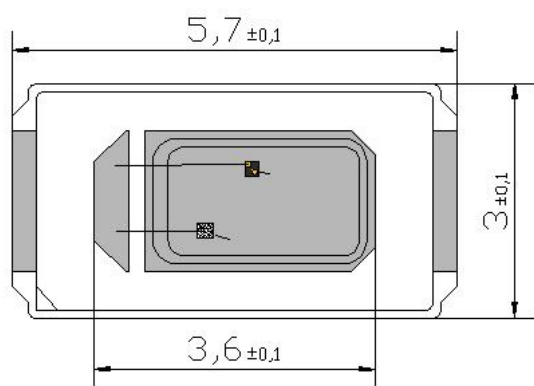


Product conformation:

- Packaging glou: 5730 encapsulation
- Luminous color: red wavelength 660nm+Zener
- Chip specification: 8mil*8mil
- Luminous Angle: 22 degrees
- Electrostatic sensitive material



Package outline dimensions



Pad Configuration

Pad	Function
1	Anode
2	Cathode
3	Cathode

Note:

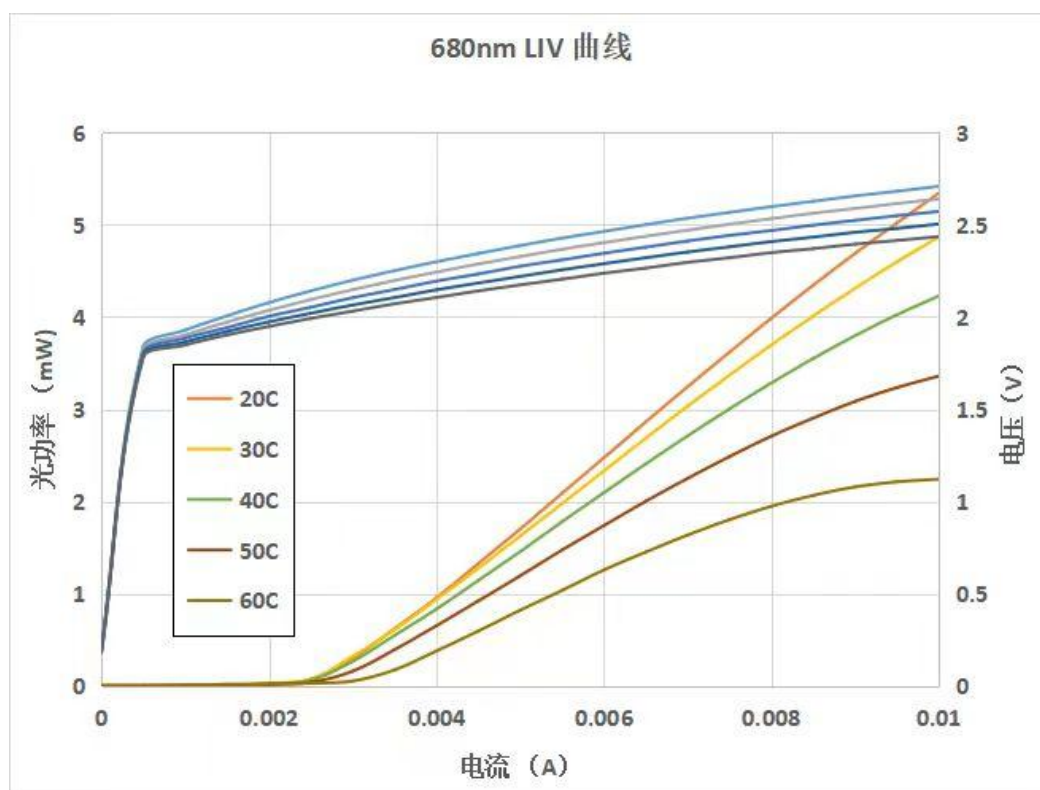
1. Dimensions are in millimeters.
2. Tolerances unless mentioned are ± 0.1 mm.

Optical Characteristics

Parameter	Symbol	Unit	Min.	Typ.	Max.	Note.
Threshold Current	I _{th}	mA	-	2.45	2.9	--
Forward Voltage	V _F	V	2.3	-	2.6	I _f =10mA
Output Power	P _{op}	mW	5.0	-	7.0	I _f =10mA
Wavelength	λ _{op}	nm	675	680	685	--
Divergence Angle(1/e ²)	deg	°	-	19	23	I _f =10mA

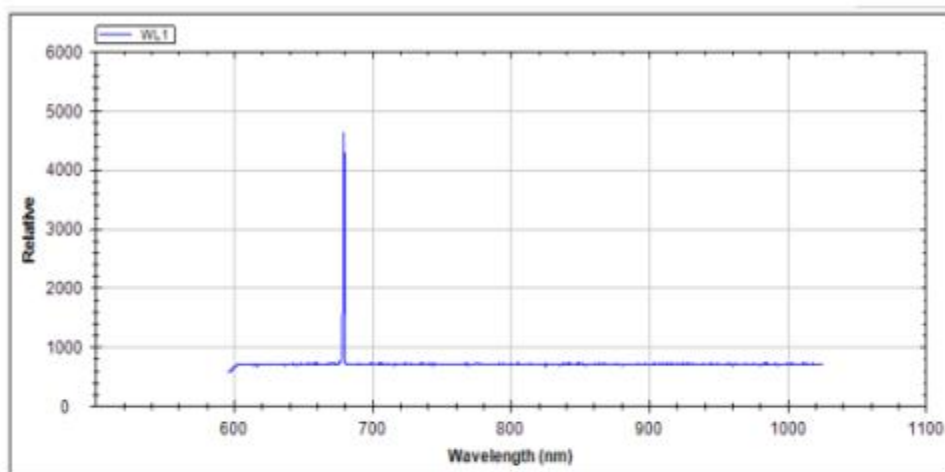
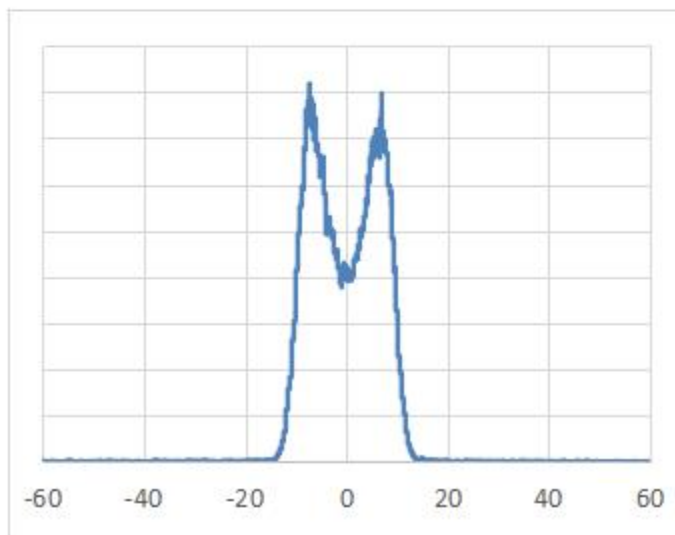
25℃, QCW Model (pulse length 0.5ms,1%Duty Cycle)

Typical Electro-Optical Characteristics Curves



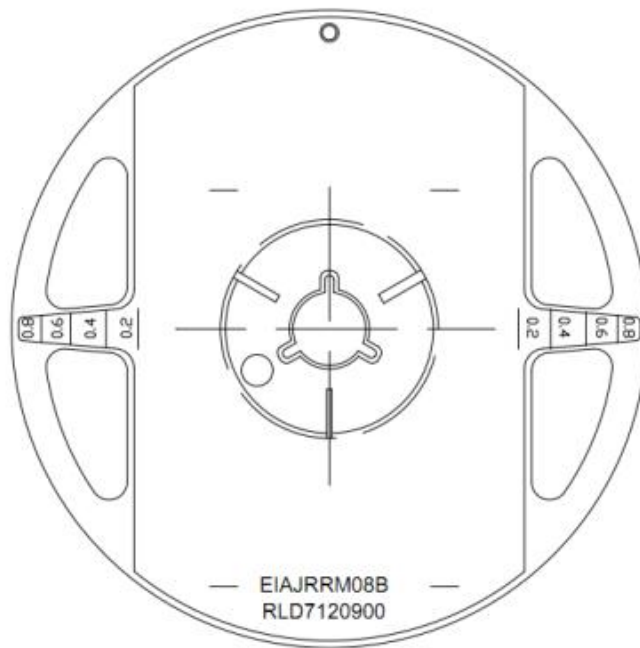
Wavelength & Divergence Angle

光谱

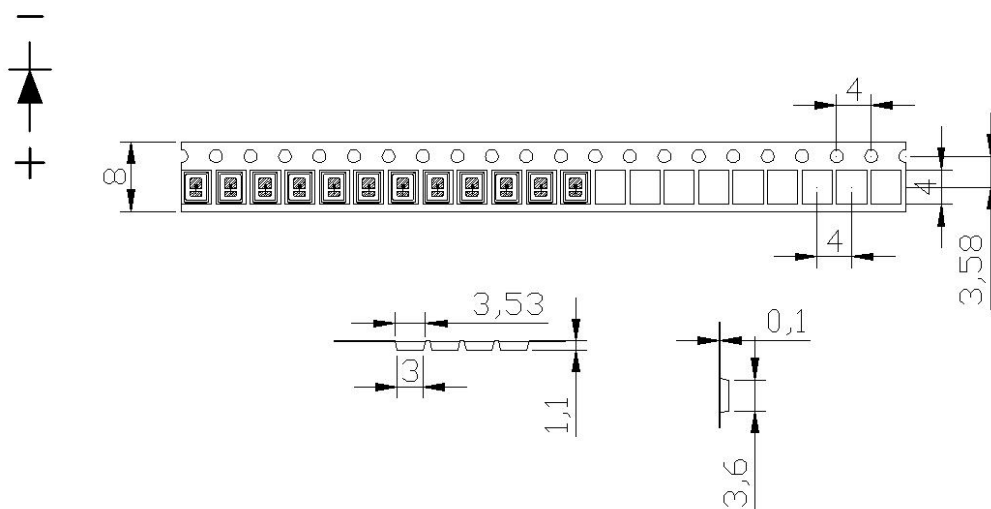
远场发散角 ($\sim 22^\circ$ at 10mA)

Technical drawing of a vertical component with the following dimensions:

- Overall height: 178.0 ± 1
- Top flange width: 1.4
- Distance from top flange to first internal feature: 56.0
- Distance between internal features: 60.0 ± 0.5
- Internal feature width: 2.0
- Bottom flange width: 12.5 ± 0.5
- Bottom flange thickness: 13.5 ± 0.5



Loaded quantity 4000PCS per reel



Application precautions

Preservation and use

1. Before opening the package, it should be stored at 30°C/60%RH or less. After opening the package, it should be placed in an environment of 20-30°C/30%RH or less.
2. To avoid the impact of the environment, it is recommended to dehumidify after unpacking at 80°C/24H. All the VCSELs are needed to be vacuumed to avoid failure
3. If the desiccant faded or expired use, dry baking: 80±5°C/24 hours.
4. VCSEL Glue surface easy to dust, need to do the relevant dust prevention measures.

Pick and place

When taking the VCSEL, you should only touch the bracket. Tools such as tweezers should not put pressure on the lens. Don't stab or push the lens.

Heat treatment

When driven by excessive current, the Tj (node temperature) of VCSEL will exceed the period limit value, which leads to a serious shortening of VCSEL life. Thermal treatment measures should effectively reduce the thermal resistance of application products. Common practice: install VCSEL packages on metal matrix PCB boards. 1W VCSEL products require the surface heat dissipation area of the metal substrate to be at least 30cm squared (over 80cm squared is recommended for 3W products), and its thermal conductivity is higher than 2.0w /mK. VCSEL and gold substrate are combined by thermal conductive adhesive with good thermal conductivity. The thermal conductivity coefficient is required to be higher than 1.0w /mK and the thickness is less than 100um.

Clean

If you need to clean, use a clean, soft cloth dipped in alcohol to gently remove foreign matter. Do not use a cleaner such as acetone to avoid possible corrosion damage.

Electrical precautions

1. VCSEL Reverse drive is not allowed
2. Current limiting measures are necessary, otherwise slight voltage changes will lead to large current changes, which may lead to VCSEL failure.
3. Under the premise that the luminous quantity meets the requirements, it is recommended to use the drive current lower than the rated current, which is conducive to improving the reliability of the product.

Anti-static precautions

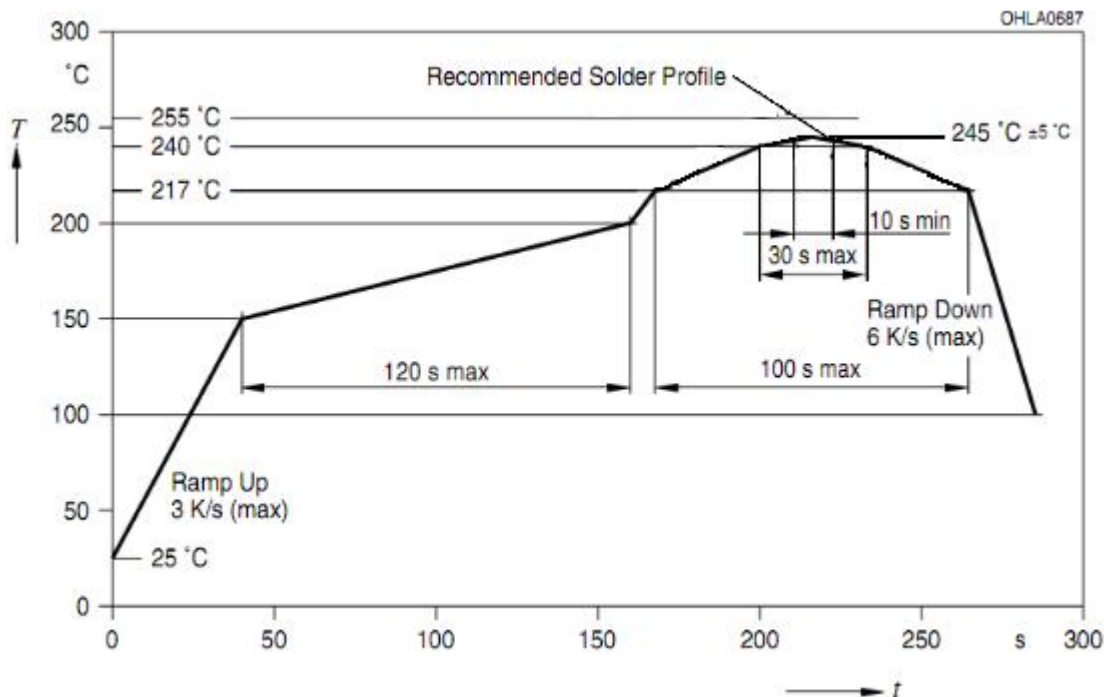
VCSEL is electrostatic sensitive devices, in the process of preservation, use to take anti-static measures. Static electricity and surge can lead to changes in product characteristics, such as forward voltage reduction, which can be serious and even damage the product. So for the whole process (production, testing, packaging, etc.) and VCSEL direct contact staff should do a good job to prevent and eliminate electrostatic measures. All related equipment and machinery should be properly grounded. The grounding ac resistance is less than 1.0 ohm, the table mat with surface resistance of 10⁶-10⁹ ohm is needed on the work table. Ion fans must also be installed in electrostatic environments and equipment. During the operation, the operator should use anti-static bracelet, anti-static mat, anti-static overalls, working shoes, gloves, anti-static capacity.



An electric soldering iron

It is recommended to use anti-static electric soldering iron, the temperature at the tip does not exceed 350°C, less than 3 seconds for each soldering. The power of the soldering iron should be less than 60W. Weld two electrode pins more than 2 seconds after each welding. Do not force the lens during welding. Problems with VCSELs usually begin when they are soldered. So you must work carefully as required.

Reflow instructions



Precautions

1. Reflow soldering is only allowed once.
2. Do not apply pressure to the lamp body during reflow
3. After reflow welding is completed, do not press the heat dissipation plate, do not press to the colloidal part.
4. If there is a lower melting point of solder paste, TP can be appropriately reduced.